

# LVD96 "LVDS" DRIVER/RECEIVER SPECIFICATIONS page 1 of 1

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**UNIBLITZ**<sup>®</sup>  
BY VINCENT ASSOCIATES

## FEATURES

- Signal conversion from LVDS to TTL
- Signal conversion from TTL to LVDS
- DB9 modular "in-line" design for cable connection
- 6-pin pluggable terminal block with spring-cage connection for wires
- BNC Jack for single TTL input signal
- BNC Plug for single TTL output signal
- Pin-to-pin compatible with CAMELIA "Data/Synchro." cable (D-sub 9-pin connector end)
- Power input: +5VDC at 50mA - user provided.
- Size (HWD): 0.67 x 1.22 x 2.72 inches (17.0 x 31.0 x 69.0 mm.)
- Wire length: 6.50 inches (approx.)
- Weight: 2.12 oz. (60.0g.)
- Price - \$225.00 (Domestic)  
\$235.00 (Foreign)

### P1 Connections:

- (2.5 mm 6-pin spring-cage plug)
- #1 - Red Wire (22 AWG) for +5V
  - #2 - Black Wire (22 AWG) for GND
  - #3 - BNC Plug Signal Wire
  - #4 - BNC Plug Ground wire
  - #5 - BNC Jack Signal Wire
  - #6 - BNC Jack Ground Wire

### H1 Connections:

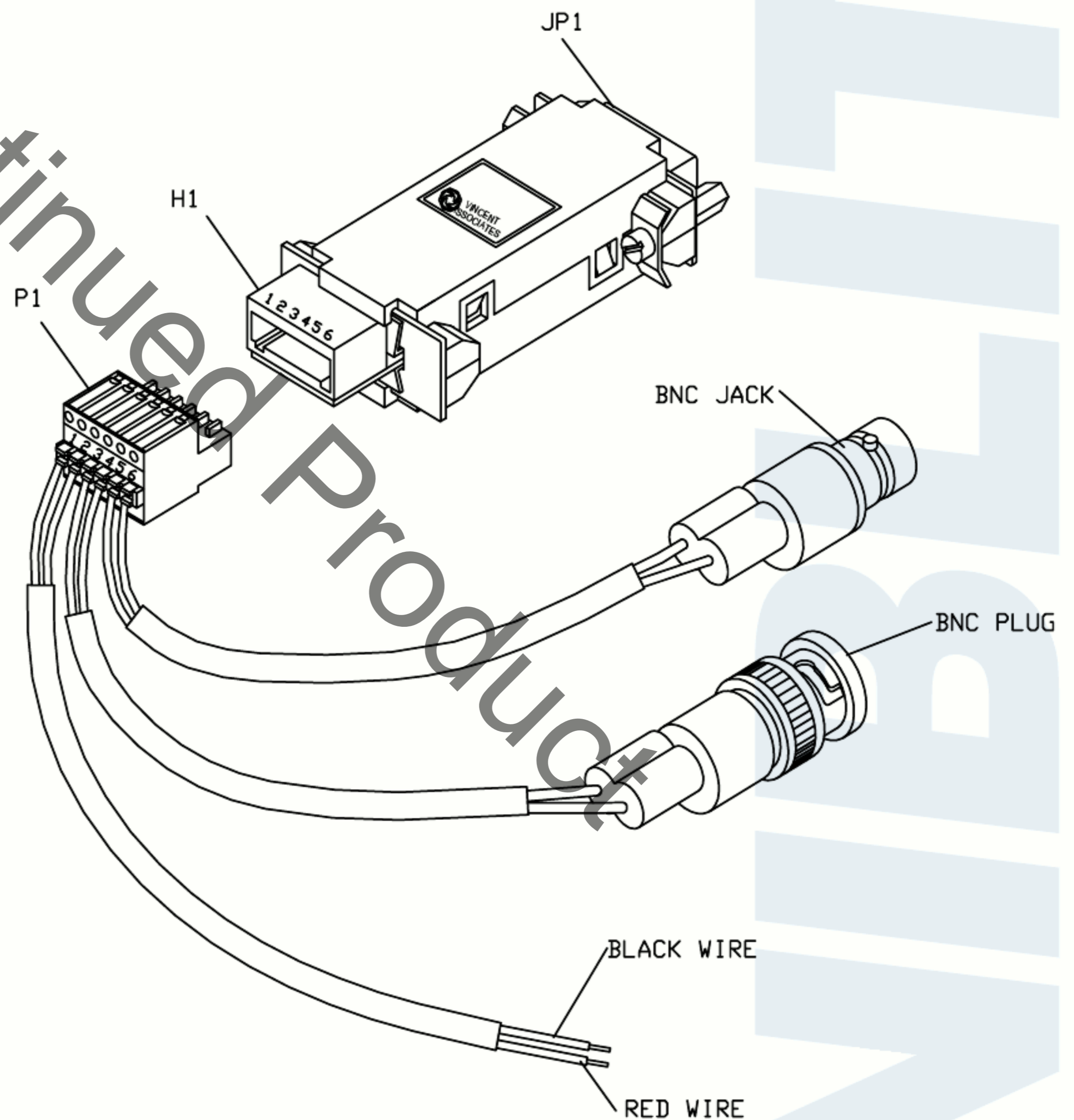
- (2.5 mm 6-pin horizontal header)
- #1 - +5VDC Power Supply input
  - #2 - Power Ground
  - #3 - TTL Output - active high
  - #4 - Signal Ground
  - #5 - TTL Input - active high
  - #6 - Signal Ground

### JP1 Connections:

- (D-sub 9-pin Female connector)
- #1 - LVDS (+) Pulse Input
  - #2 - LVDS (+) Trigger Output
  - #3 - N/C
  - #4 - N/C
  - #5 - Ground
  - #6 - LVDS(-) Pulse Input
  - #7 - LVDS(-) Trigger Output
  - #8 - N/C
  - #9 - N/C

## GENERAL DESCRIPTION

The LVD96 is an electronic adapter for applications requiring an interface between LVDS (Low Voltage Differential Signaling) and TTL (Transistor Transistor Logic) signal levels. The unit operates from a +5 VDC external power supply (not included) as found with a UNIBLITZ VMM-D1 or VMM-T1 shutter-driver controller. BNC connectors (one Jack and one Plug) provided for quick termination of TTL command signals. LVDS I/O signals are available via the DB9 female connector.



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