

FEATURES

- Innovative patented drive circuitry utilizes constant current control of UNIBLITZ® shutter actuator.
- Open frame printed circuit card suitable for OEM applications. (See Figure 1)
- Power input: +24VDC at 1.5A - user provided.
- Pluggable 12 position I/O connector.
- Easily interfaces with electronic synchronization system.
- On-board jumper allows user to select shutter used.
- Output provided for electronic synchronization system.
- Shutter interconnect cable included. (710P 7-pin female to 7-wire pigtail, 10ft.)
- Exposure determined by external pulse source for pulse width determined exposure time.
- Size (HWD) 1.0 x 3.3 x 4.0 inches (25.4 x 84.5 x 101.6 mm)
- Weight 3 oz. (.09 Kg.)
- Price - \$310.00 (Domestic)

The D880C is a state-of-the-art shutter driver circuit utilizing constant current control which has shown to increase shutter life time over standard capacitive discharge types. The D880C's versatility allows it to operate Vincent UNIBLITZ shutters. The unit operates from a user supplied +24VDC power supply. It can be easily integrated into OEM applications where +24VDC is available.

One feature of the D880C is the pluggable input/output screw terminal connector for quick removal of the board without having to remove any wires connected to the terminal block.

Another feature of the D880C is an on-board jumper that allows the user to select the shutter to be operated.

Supplying a TTL (5V) logic level to the trigger input or connecting a mechanical or electronic switch between the trigger input and +6.75VDC output is all that is required to control the D880C.

D880C Printed Circuit Card layout and clearance outline. Numbers (1-12) show input/output connection placement. Captive screw portion of input/output connector is removable.

Notes

1. The D880C will need to be user preset to the shutter used in your particular application. Shutters used with the D880C will require installation of the "E" stop selection. This will provide a shutter system capable of achieving increased shutter life time.
2. See User Manual for cable (710P) layout and P1 header connections.

